Elec 422: VLSI Design I

Project Milestones

Fall 1997

Sept. 2 - 12	Group Formation
Sept. 16	Initial Project Idea
Sept. 16 - 19	Project Discussion during Office Hours
Sept. 23	One to Two Page Functional Description and Start of WWW page
Sept. 23 - 26	Project Discussion during Office Hours
Sept. 30	Initial Logic and Algorithm Schedule Due
Sept. 30 - Oct. 3	Project Discussion during Office Hours
Oct. 7	PLA, Control and Timing Description Due
Oct. 7 - 10	Project Discussion during Office Hours
Oct. 14	Initial Circuit Layout Due
Oct. 14 - 17	Project Discussion during Office Hours
Oct. 23	Project Status Report Due
Oct. 23 - 24	Project Discussion during Office Hours
Nov. 13	Mid-Term Exam, In-Class
Nov. 18 - 21	Design Review of Layout during Office Hours
Dec. 2 - 4	Design Review of Layout during Office Hours
Dec. 9	Project Presentation to Class
Dec. 10	Final Layout CIF file, Project Report, and WWW Page Due