Elec 422: VLSI Design I

Project Milestones

Fall 1998 - Updated

Sept. 1 - 10	Group Formation
Sept. 10	Initial Project Idea
Sept. 10 - 11	Project Discussion during Office Hours
Sept. 15	One to Two Page Functional Description and Start of WWW page
Sept. 15 - 18	Project Discussion during Office Hours
Sept. 22	Initial Logic and Algorithm Schedule Due
Sept. 22 - Oct. 2	Project Discussion during Office Hours
Oct. 6	PLA, Control and Timing Description Due
Oct. 6 - 9	Project Discussion during Office Hours
Oct. 13	Initial Circuit Layout Due
Oct. 13 - 15	Project Discussion during Office Hours
Oct. 27	Project Status Report Due
Oct. 27 - Nov. 6	Project Discussion during Office Hours
Nov. 12	Mid-Term Exam, In-Class
Nov. 17 - 20	Design Review of Layout during Office Hours
Dec. 1 - 4	Design Review of Layout during Office Hours
Dec. 8	Project Presentation to Class
Dec. 9	Final Layout CIF file, Project Report, and WWW Page Due
Dec. 16	Final Layout CIF file Due at MOSIS