Elec 422, VLSI Design I
Fall 1998

Class Presentation Guidelines

November 12, 1998

Presentations: December 8, 1998, Evening

Presentation Format

Each group will have less than 10 minutes to make their presentation to the class. The group should prepare a “poster” which describes the project. The poster will be limited to two pieces of posterboard which will be supplied. Each piece of posterboard is 22×28 inches. To show sections of the layout, please you use $cif2ps$, $pplot$, xv, or xwd plots as part of the poster.

Organization of Presentation

Each member of the group should discuss the section which is their respective specialty. Due to the limited amount of time, you should present the key, unique features of your design. The following topics should be presented:

1. Functional Description of Chip
2. Circuit Design
   - Logic Diagrams
   - System Timing Diagrams
3. PLA Description
   - State Diagram
   - Input to meg
4. Layout
   - Plots of low-level Cells

- Cell Hierarchy
- Floorplan
- Full plot of Chip

5. System Performance Analysis

    - Crystal Analysis of Longest Path
    - Spice Analysis of Critical Sub-Circuit