Elec 422: VLSI Design I
Fall 1998
November 12, 1998
Final Report Format

Due: December 9, 1998

The final report is a group report and will represent about half of your grade. Please be thorough in your presentation. Your report should contain the following sections and will be weighted accordingly. It is important that you indicate in the closing section, the division of work by the group members.

Project Web Page

You may choose to do your report as a traditional paper report or in electronic format through extensions and additions to your Elec422 course project Web page. If you choose the electronic format, please keep all html and graphics files in one directory and make all Web links relative to the current page, so that the report can be easily moved to the www.ece.rice.edu server during summer 1999. In any case, the course project Web page should be reasonably complete and give a thorough overview of the design.

Report Subsections

1. Functional Description (10 points):
   Pin-map Showing Signal Name and Physical Pin Number:
2. Circuit Design (25 points):
   Logic Diagrams:
   System Timing Diagram:
   Irsim Results of Subcells:
   Irsim Results of Entire Chip:
3. PLA Description (10 points):
   Input to meg:
   Irsim Simulation:
4. Circuit Layout (30 points):
   Plots of low-level Cells (using $cif2ps$ or $pplot$):
   Cell Hierarchy:
   Floorplan:
   Full plot of Chip (using X window dump or xv):
5. Performance Analysis; Maximum clock frequency (15 points):
   Crystal Analysis of longest path (5 points):
   Spice Analysis of critical sub-circuit (10 points):

6. Summary
   References
   Division of Work by Group Members
   Comments and Suggestions on CAD tools

7. Format and style (10 points):