Elec 422, VLSI Design I
Fall 1999

Class Presentation Guidelines

AMD Design Contest Presentations

Presentations: DH1049, December 7, 1999, 2:30-5:00pm

Presentation Format

Each group will have about 10 minutes to make their presentation to the class. The group should prepare a presentation with overheads or with Powerpoint or Postscript/PDF which describes the project. To show sections of the layout, please you use $cif2ps$, $pplot$, xv, or xwd plots as part of the presentation. I will have a laptop and video projector available.

Organization of Presentation

Each member of the group should discuss the section which is their respective specialty. Due to the limited amount of time, you should present the key, unique features of your design. The following topics should be presented:

1. Functional Description of Chip
2. Circuit Design
   - Logic Diagrams
   - System Timing Diagrams
3. PLA Description
   - State Diagram
   - Input to meg
4. Layout
   - Plots of low-level Cells

- Cell Hierarchy
- Floorplan
- Full plot of Chip

5. System Performance Analysis
   - Crystal Analysis of Longest Path
   - Spice Analysis of Critical Sub-Circuit