

ELEC 243
Problem Set 7
Exam Section
Due: March 13, 2015

Exam Cover Sheet

Exam problems begin on the next page.

There is one problem this week. It has a two hour time limit.

Before beginning you should read the following guidelines:

- All exam problems are pledged work.
- You may not discuss any problem with anyone other than the instructor.
- Unless otherwise specified, you **MAY** utilize:
 - A calculator.
 - Your textbook, class notes, and any material on or *directly* linked to the class web page.
 - A table of integrals.
- Unless otherwise specified, you **MAY NOT** utilize:
 - Matlab or any other computer software.
 - Any reference material other than those listed above.
- “Find” means to determine a numerical value or an algebraic expression for the indicated quantity or function. “Sketch” or “plot” means to draw a graph of the indicated function, not necessarily to accurate scale, but with labeled axes and important values indicated.
- Some problems will have a time limit. This will be indicated at the beginning of the problem itself and on the cover sheet. If no time limit is specified, you may use as much time as desired, subject to the solution being handed in on time.

When you are ready to begin, turn the page.

E7.1 This problem has a two hour time limit.

You have been chosen to represent Rice in the National Op-Amp Design Contest. Each contestant is given a box containing the following components: 2 op-amps, 2 15 V batteries, 1 diode, a 1 μF capacitor, and a coupon good for 6 resistors of any value. Your task is to build a circuit which will perform each of the following functions. (v_1 , v_2 , v_{in} , and i_{in} are arbitrary, external, ground referenced voltages and currents which will be provided by the judges.)

The contest rules: The input and output voltage and current terminals are defined to be '+' with respect to ground. The sign of the output must be correct. You may reuse components from one circuit to the next. However, you must build all four circuits from the same pool of 6 resistors.

(a) $v_{out} = 7v_{in}$

(b) $v_{out} = 3v_1 - 4v_2 + 5$

(c) $v_{out} = -10 \int_{-\infty}^t v_{in}(\tau) d\tau$

(d) $v_{out} = 1500i_{in}$ (v in volts, i in amperes)