Pin Description

CLKA and CLKB: These are the input pins for the two phase clocking used.

Pdork: This pin specifies whether a key or data is going to be entered through the input pins.

Penable: This pin must be high for either the key or data to be entered.

GND: This pin is ground for the internal circuitry of the chip.

GND_IO: These pins are the grounds for the input and output pads.

Pin0 through Pin7: These are the input pins for a 14-bit key and a 16-bit data set. Both the key and the data are entered serially in two parallel blocks of 7 or 8 bits respectively.

Popt_valid: This pin in high when the output of the chip is valid.

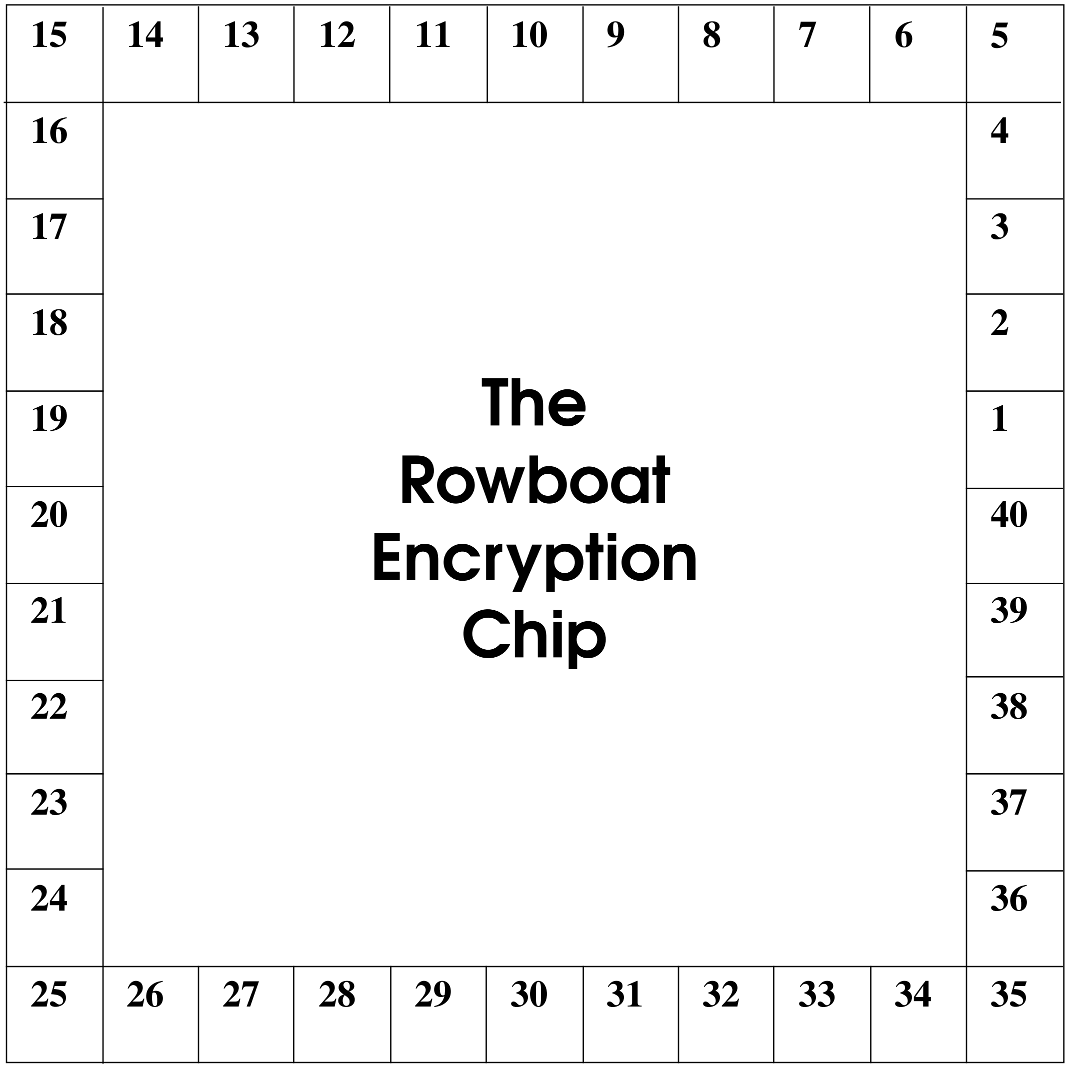
Pout0 through Pout7: These are the output pins for a 16-bit data set, which is pouted in two serial blocks of 8 bits.

Prestart: This pin will restart the chip, flushing all data.

Vdd: This pin supplies power to the internal circuitry of the chip.

Vdd_IO: These pins are the power supplies for the input and output pads.

Pin Map



1. Pout2
2. Pout1
3. Pout0
4. NC
5. Vdd_IO
6. NC
7. NC
8. NC
9. NC
10. GND
11. NC
12. Popt_valid
13. NC
14. NC
15. Vdd_IO
16. CLKB
17. Prestart
18. Pdork
19. CLKA
20. Penable
21. Pin0
22. Pin1
23. Pin2
24. Pin3
25. GND_IO
26. Pin4
27. Pin5
28. Pin6
29. Pin7
30. Vdd
31. NC
32. NC
33. NC
34. NC
35. GND_IO
36. Pout7
37. Pout6
38. Pout5
39. Pout4
40. Pout3