```
*so far, b3 is longest.
* vlsi project - find longest path in alu
*12/11/97

* Set BASIC VOLTAGE levels
vdd <Vdd> <GND>  dc 5
* set substrate voltages : P-sub = Vdd;  N-sub = GND
vs1 <CMOSP> <Vdd>  dc 0
vs2 <CMOSN> <GND>  dc 0

* ------------------------------------

* Set other constant circuit inputs, for example b, cin:
* None here.
vxo  <x1> <GND> dc 0
vxt  <x2> <GND> dc 0
vxth  <x3> <GND> dc 0

vyz <y0> <GND> dc 5
vyo <y1> <GND> dc 5
vyt <y2> <GND> dc 5
vyth <y3> <GND> dc 5

virz <lsb> <GND> dc 0
viro <mid> <GND> dc 5
virt <msb> <GND> dc 0

vclk <clka> <GND> dc 5

vrbus <readbus> <GND> dc 0
vralu <readalu> <GND> dc 5
vcl <clearacc> <GND> dc 0
vld <ldacc> <GND> dc 5
ven <enacc> <GND> dc 0
vbr <buffread> <GND> dc 0


* ------------------------------------

* Set Circuit Input which will change, for example a:
* input pulse between node and GND (initially 0 ) of:
* pulse (init_value pulse_value delay rise_time fall_time pulse_width period)
vxz <x0> <GND> 0 pulse(0 5 0ns 0.1ns 0.1ns 10000ns 10ns)


* ------------------------------------



* Do analysis: give increments and total time for analysis.
.tran .1ns 50ns

* If running in batch mode spice -b, then ascii plots are made
* Plot Voltages
.plot tran v(<y0>) v(<x0>) v(<b0>) v(<b1>) v(<b2>) v(<b3>) v(<co3>)

* set hcopydevtype=postscript

.end
```