ELEC 423: VLSI Systems II
Spring 2006 Course Syllabus

1 Organization
- Instructor: Yehia Massoud, AL A127, x6706, massoud@rice.edu
- Time and Location: Biweekly on Wednesday 4:00 – 5:00 in either DH 1046 or AL A116
- Course Web Page: http://www.ece.rice.edu/~abnieu/elec423/
- Office Hours: By Appointment

2 Required Textbooks
- Notes and tutorials on class web site

3 Class Meeting Schedule
- 1/11 – Organizational Meeting (DH 1046)
- 1/25 – Introduction to FPGA Design Flow and HDLs (DH 1046)
- 2/8 – FPGA Design Tools and ModelSim (AL A116)
- 2/22 – FPGA Hardware Compilation and Testing (AL A116)
- 3/8 – VLSI Test Equipment (AL A116)
- 3/29 – VLSI Test Methodologies I (DH 1046)
- 4/12 – VLSI Test Methodologies II (DH 1046)
- 4/26 – FPGA Design and Implementation Student Presentations (DH 1046)

4 Important Deadlines
- Homework
  - Homework 1 – Out in class on 2/8; Due in class on 2/22
  - Homework 2 – Out in class on 2/22; Due in class on 3/8
  - Homework 3 – Out in class on 3/8; Due in class on 4/12
- Student Presentations on FPGA Design, Implementation and Testing – In class on 4/26

5 Course Grading
- Homework 1 – 20%
- Homework 2 – 30%
- Homework 3 – 35%
- Final Presentation – 15%

* Any student with a documented disability needing academic adjustments or accommodations is requested to speak with us during the first two weeks of class. All discussions will remain confidential. Students with disabilities should also contact Disability Support Services in the Ley Student Center.