ELEC 326: Digital Logic Design

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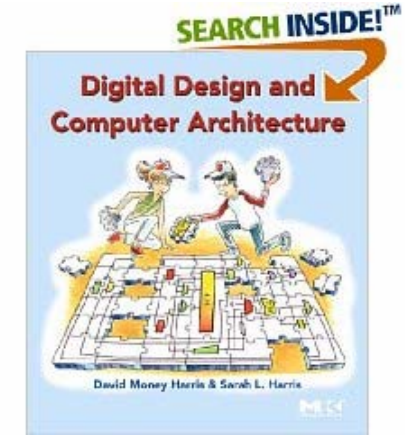
Welcome

- ELEC 326: Digital Logic Design
  - ELEC 220 pre-requisite
- Instructor
  - Kartik Mohanram
  - www.ece.rice.edu/~kmram
- ELEC 326 – www.ece.rice.edu/~kmram/elec326
  - Owlspace?
- Mailing list
  - Will be setup once I obtain final roster
  - Emails to class will be archived on web-page

Administrivia, etc.

- Office hours
  - 2:00pm to 3:00pm in DH 3029 on Tue and Thu
  - By appointment – email works best
  - Stop by after 2:00pm and take your chances
- Course information handout will be ready next class, following our discussions today
- Text-book: **Digital Design and Computer Architecture** by David Harris and Sarah Harris
  - http://www.amazon.com/Digital-Design-Computer-Architecture-Harris/dp/0123704979
  - Buy online (cheaper)
  - I will help till your copy arrives

Textbook



Course format

- Homework assignments (6): 15%
  - Supported by tutorial sessions in the evenings
  - Please see course schedule for room assignments
  - Usually Tue+Wed+Thu from 7:30-9:30pm
- Project: 15%
- Three exams: 60%
  - Comprehensive final
- Two/three self-scheduled (gasp!) vivas: 10%

Honor code

- Always write and sign the honor code on test/homeworks
  - http://www.ruf.rice.edu/~honor/
- Homework, tests, projects, and solutions from previous offerings of this course are off limits, under the honor code
- I will take violations very seriously
  - When in doubt, please ask!

Assistants

- TA for the class
  - Mihir Choudhury (mihir@rice.edu)
  - Will introduce him next class
- Course assistants: To be decided

Project

- Lab – AL A124
  - All of you will be issued a key in next 2-3 weeks
- 3 stages with deadlines
  - Groups of 2-3, assigned randomly for stages 1 and 2
- Hope to also kick-off AMD-sponsored design contest that is independent of project
  - Take your design further, met some desirable metrics that real-world designers seek
  - Negotiations underway

Deadlines are firm

- Homework due a week from when handed out
  - Due by 5pm in my office on Fridays
  - No extensions
- Project demos are due on specified dates per the course schedule

Exams

- Late September (28th), late November (22nd)
  - Flexible early scheduling
- Comprehensive final
- All three are timed, first two are in-class

Class participation

- Lecture slides will be handed out
- But, I will lecture mainly off the board
  - You are expected to
    - Be familiar with material already covered
    - Glance over material to be covered
- Participation includes
  - Answering questions
  - Pop quizzes
  - Solving problems on the board
  - And any other challenges that I can think of

Assignments

- HW0 out today
  - Due by 5pm tomorrow in my office
- Sign-up sheet circulating
- ELEC 220 material
  - Review chapters 1-4

Before we digress

- Pet peeve: Email expectations
- Questions? Anything that I (may) have missed?

- Time to digress
  - B. Tech., IIT Bombay, EE, 1998
  - Ph.D., UT Austin, ECE, 2003
  - Fault-tolerant computing, CAD for VLSI, etc.